

AMENDMENTS TO THE SPECIFICATION

Please replace the paragraph starting on page 9, line 22 and ending on page 10, line 1 with the following:

Although only one receiver 20 is shown in Fig. 3, frequency data generator 22 may supply frequency data constituted by digital signals to the plurality of receivers 20 that are actually provided. In addition, the signal source of the clock signal, which is generated by a clock 80, that is supplied to the digital system is common to all constituent elements including the plurality of receivers 20, and phase error therefore does not occur inside one local oscillation signal supply circuit.